VI1+
II1+
VI1-
VI2+
II2+
VI2-
MU200-4AM
PWR
RUN
ERR
VO1+
IO1+
VI3+
VO2+
IO2+
VI4-
PG

ERR	

5.2

-10000

10000

5-1

$$D = \frac{S_U - S_L}{Uu - U_L} \times (U - U_L) + S_L \qquad (5-1)$$

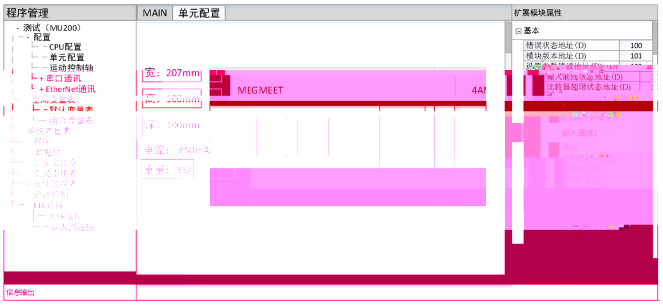
D =

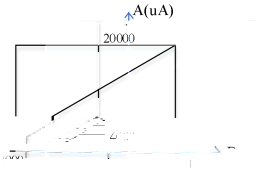
U = ( )

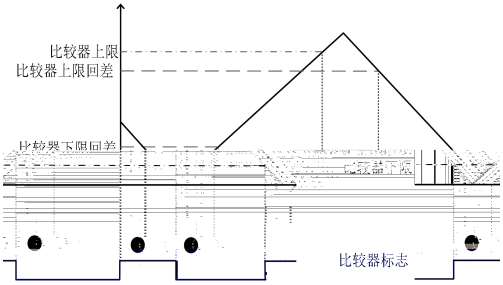
U_U = U_L =

S_U = S_L =

1 -ṅ Ḳ







1 6-2

6-2

16Bit	1 0 BIT0: BIT1: BIT8: BIT9: BIT10: BIT11:

2 ID

0 ID

3 6-3

6-3

16Bit	BIT0~3：CH1~4 0 1 BIT4~15：

4 6-4

6-4

16Bit	BIT0~3：CH1~4 0 1 BIT4~15：

5 6-5

6-5

16Bit	BIT0~3：CH1~4 0 1 BIT4~15：

6 6-4

6-4

16Bit	BIT0：CH1 BIT1：CH2 BIT3 BIT15：

7 PLC

0 0~10V 0V 4~20mA 4mA 0V

0mA

8

9 1~100 8

A/D

(D)

A/D

7.

1

2

3.

4 RUN PWR RUN

10Hz

8.

- “PWR” :

- “RUN”

10Hz

1Hz

- “ERR” :

1

2

3

4

5

6

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B 5

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